

FIG. 1

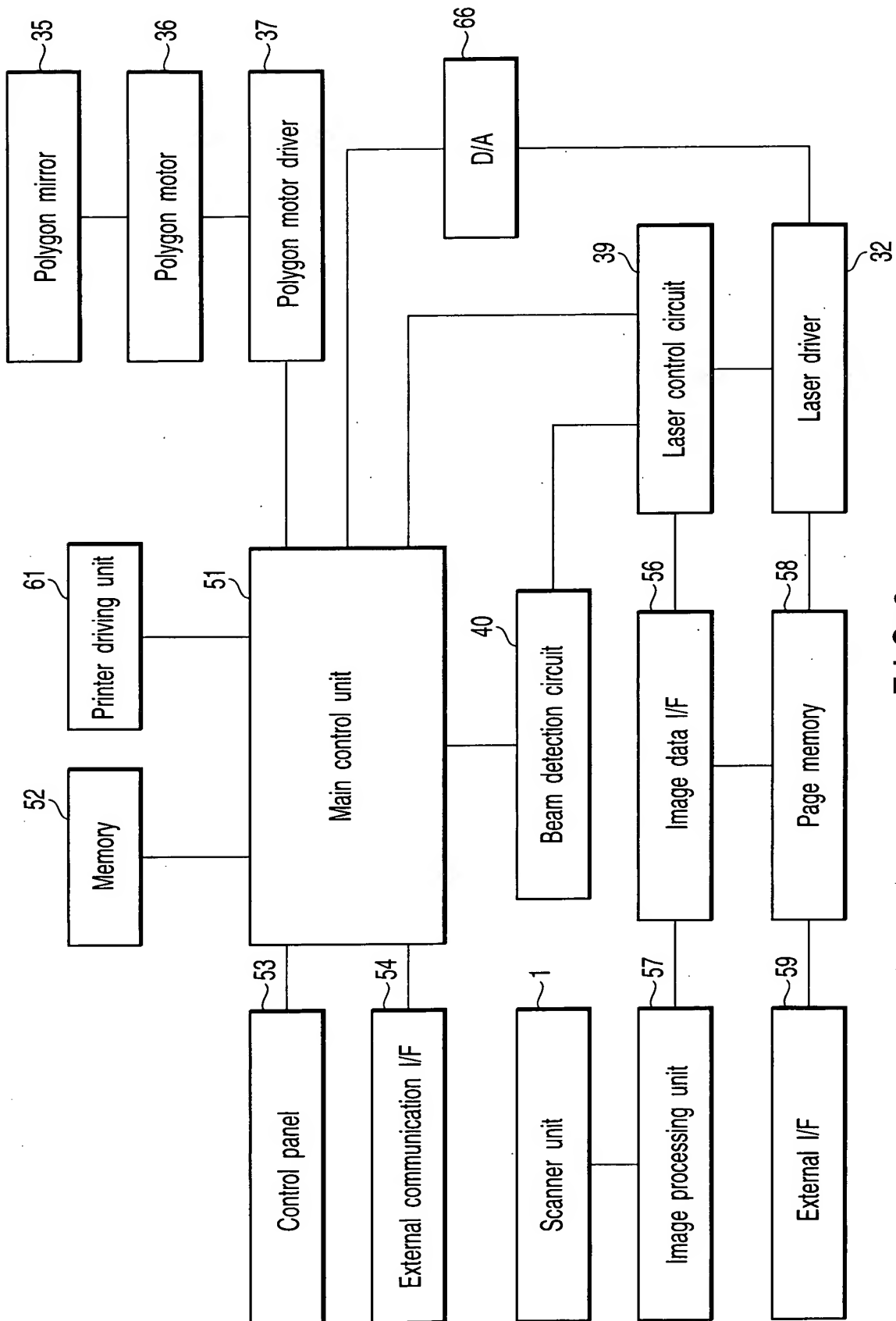


FIG. 2

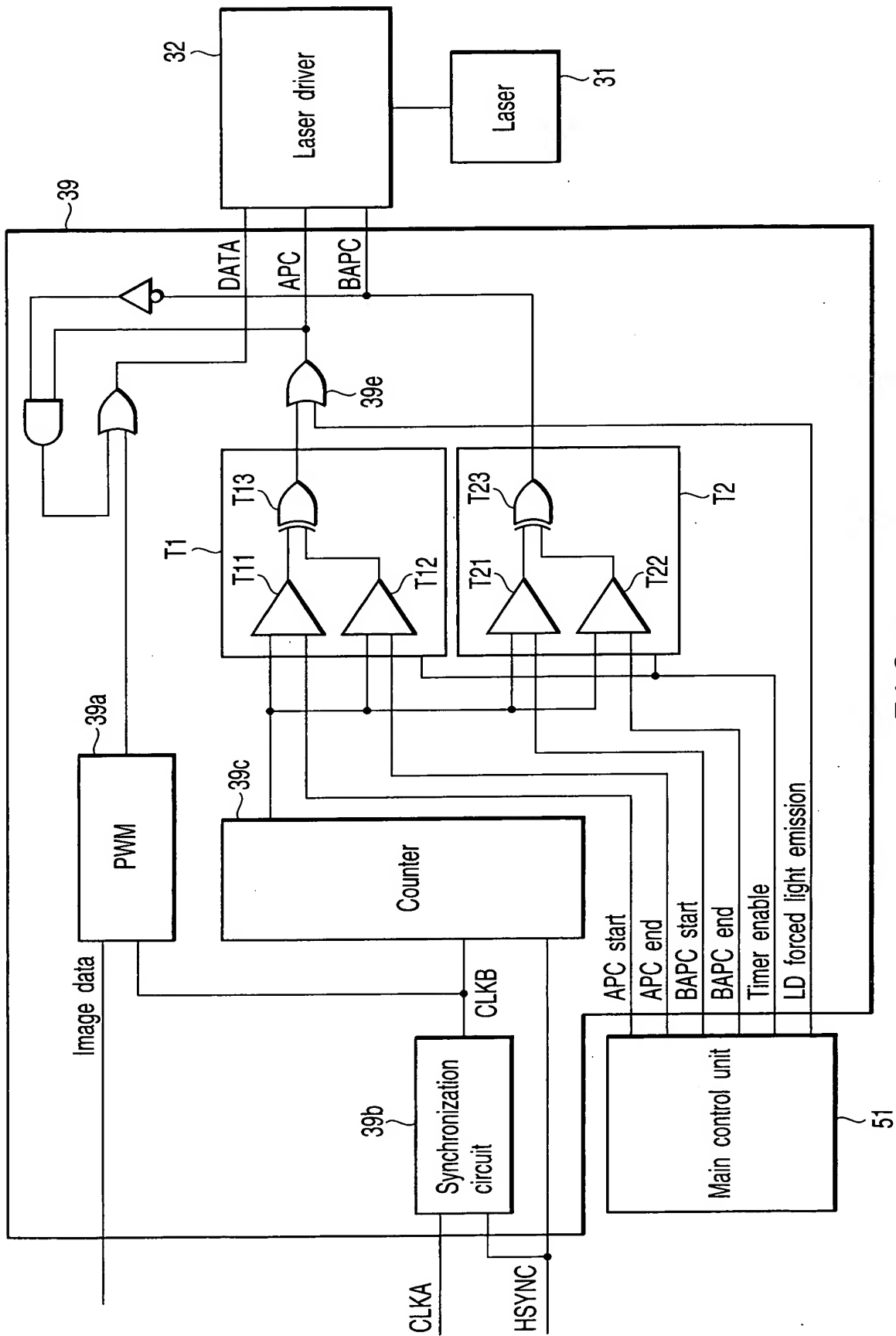


FIG. 3

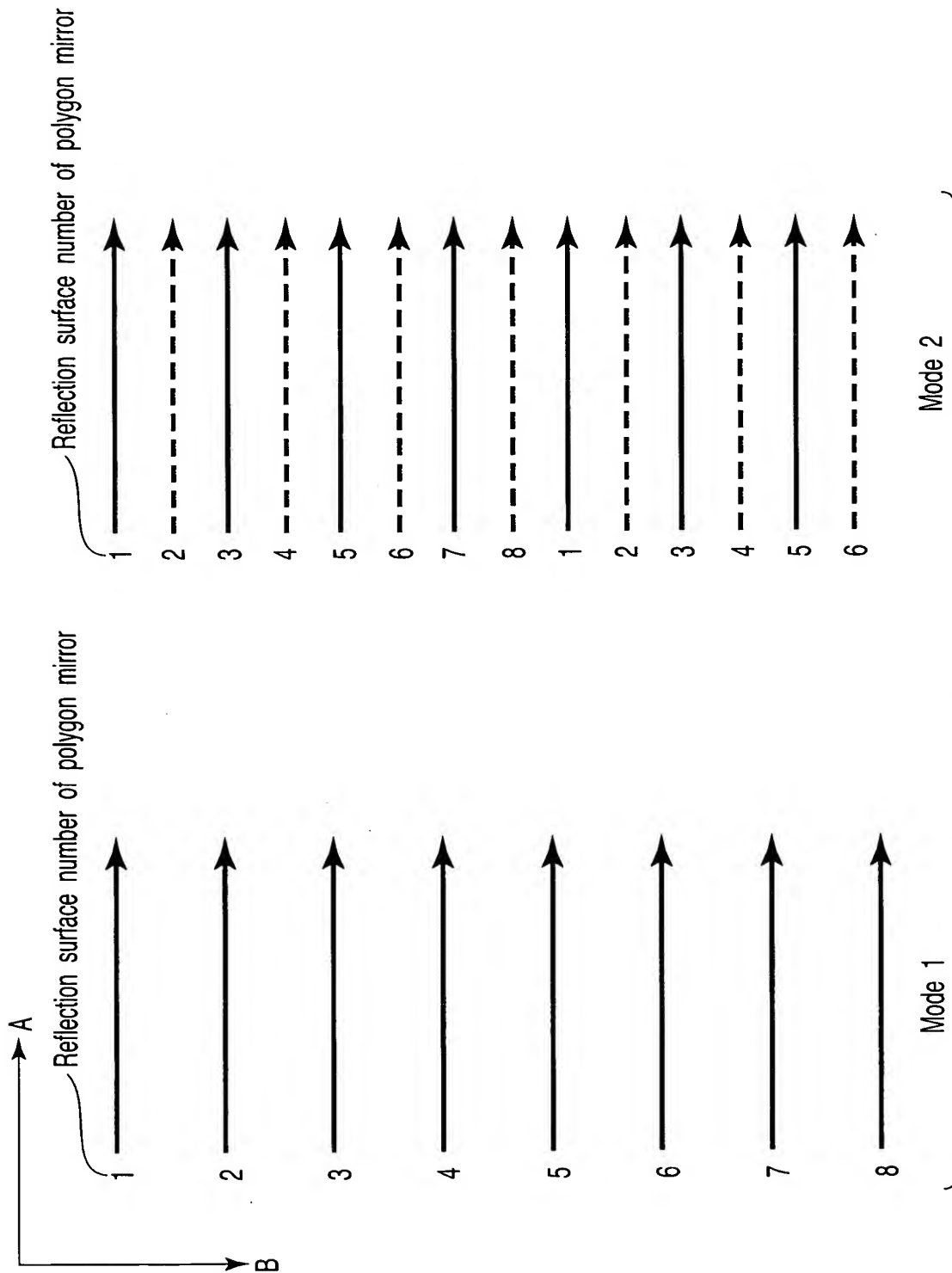


FIG. 4

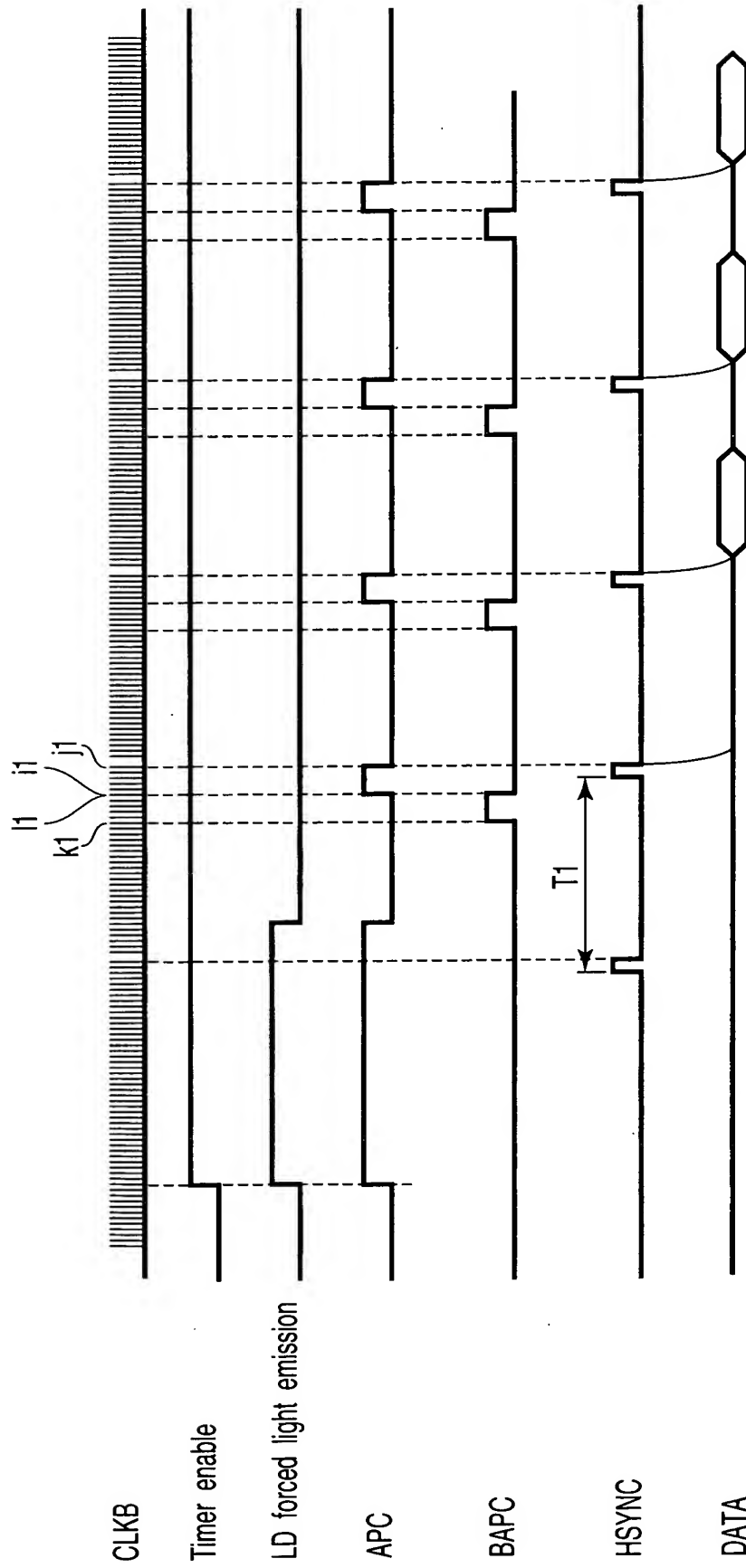


FIG.5

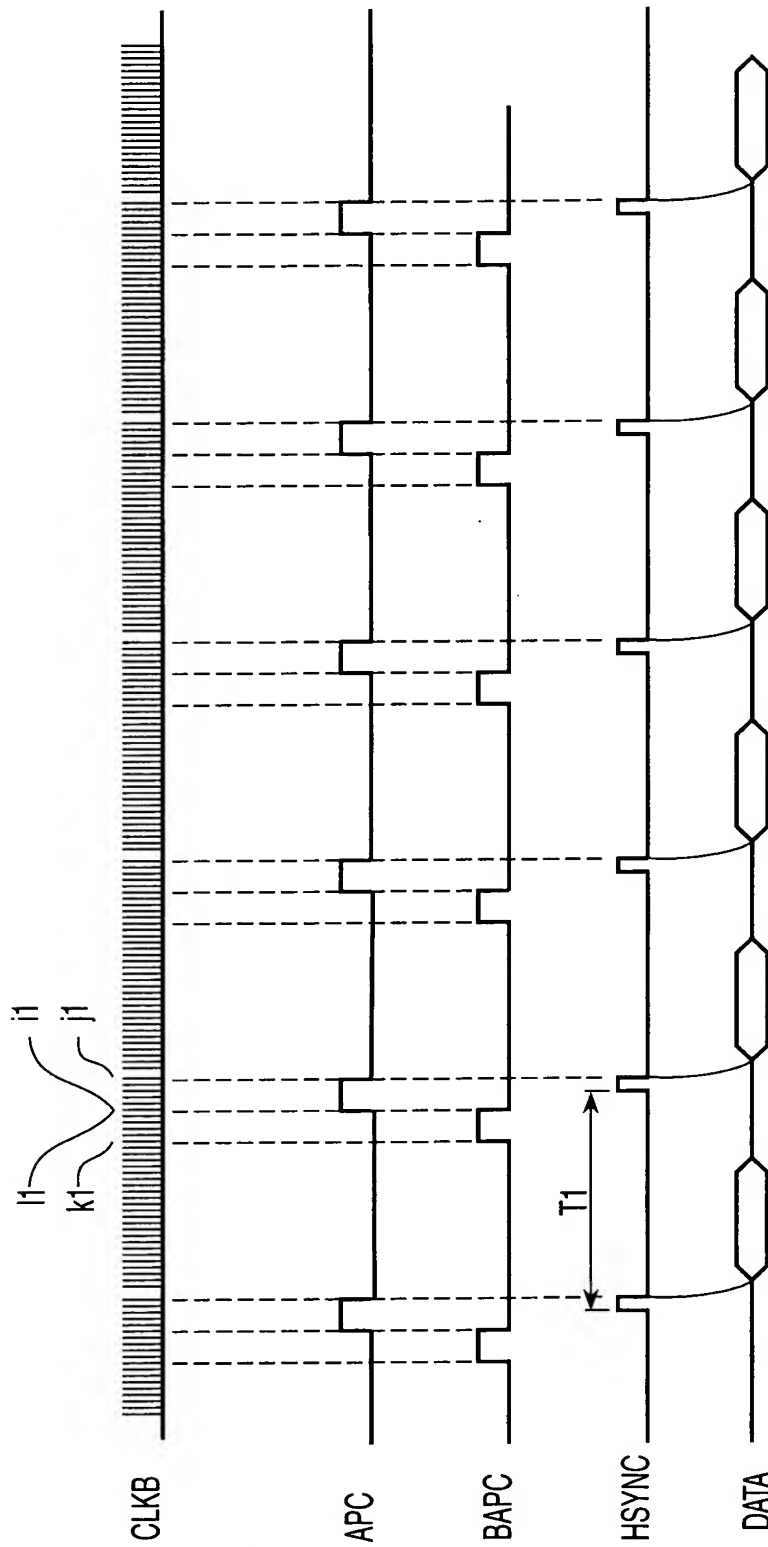


FIG. 6

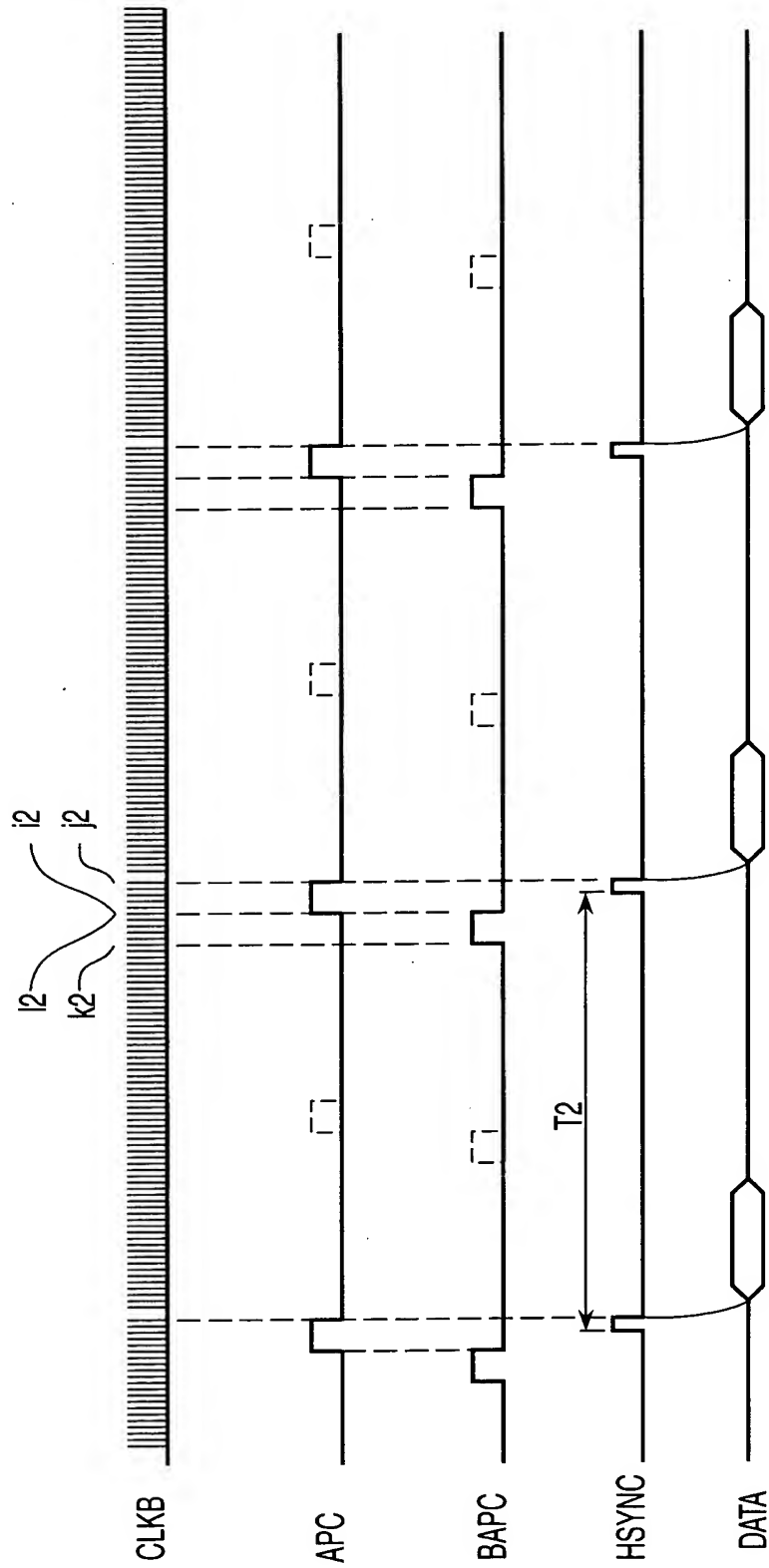


FIG. 7

	Mode 1	Mode 2
APC start	i1	i2
APC end	j1	j2
BAPC start	k1	k2
BAPC end	l1	l2

FIG. 8



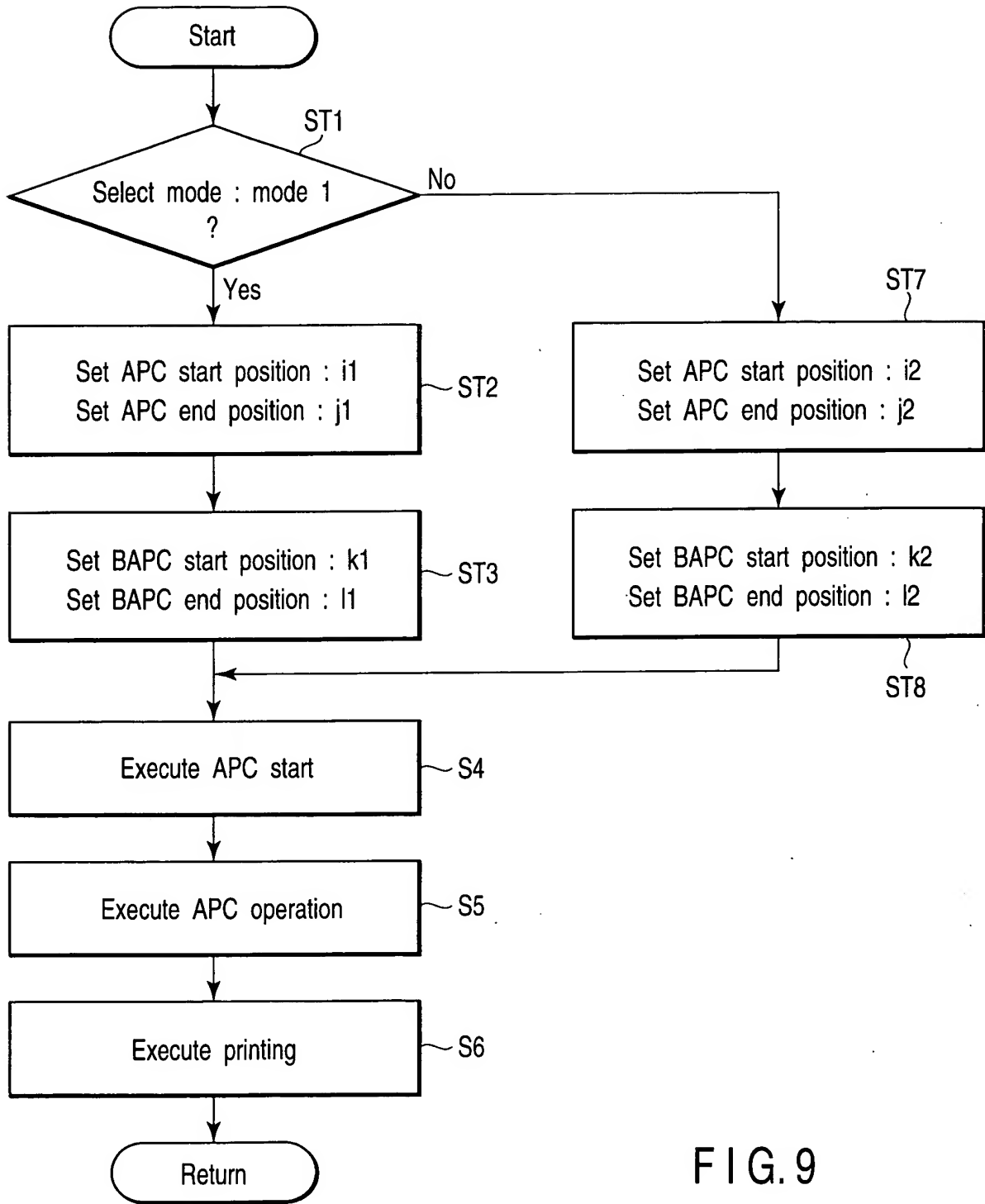


FIG. 9